

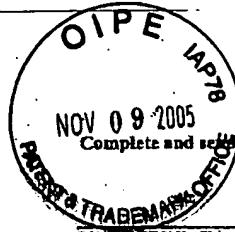
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Annette Petrus

(Depositor's name)

(Signature)

November 9, 2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/010,273	12/05/2001	Hiroaki Asada	10873.851US01	5636

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	12/21/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
NGUYEN, VAN THU T	2824	363-230010			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list:

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2. Mueller & Larson, P.C.

3. _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Matsushita Electric Industrial Co., Ltd.

Osaka, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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5. Change in Entity Status (from status indicated above)

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(a)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Date November 9, 2005

Typed or printed name Douglas P. Mueller

Registration No. 30,300

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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HAMRE, SCHUMANN, MUELLER & LARSON, P.C.

AN INTERNATIONAL INTELLECTUAL PROPERTY LAW FIRM

FAX TRANSMISSION November 9, 2005

TO: Mail Stop: ISSUE FEE
 Examiner: NGUYEN
 Commissioner for Patents
 PO Box 1450
 Alexandria, VA 22313-1450

FROM: Douglas P. Mueller
 OUR REF: 10873.0851US01
 TELEPHONE: (612) 455.3800

Total pages, including cover letter: 3

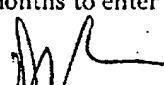
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Title of Document: **Issue Fee Transmittal – Part B, Communication regarding Title**

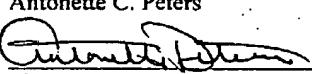
Applicant: ASADA
 Serial No.: 10/010,273
 App. Filed: December 5, 2001
 Group Art No.: 2824
 Conf. No.: 5636

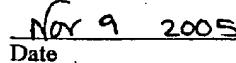
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By: 
 Name: Douglas P. Mueller
 Reg. No.: 30,300

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Antonette C. Peters


 Signature


 Date

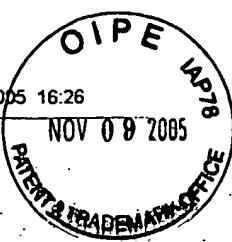
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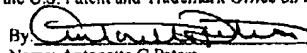
S/N 10/010,273

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	ASADA	Examiner:	NGUYEN
Serial No.:	10/010,273	Group Art Unit:	2824
Filed:	December 5, 2001	Docket No.:	10873.0851US01
Title:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on November 9, 2005.

By: 
Name: Antoinette C Peters

COMMUNICATION REGARDING TITLE

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Dear Commissioner:

Applicants respectfully bring to the attention of the Examiner that the title which appears on the Notice of Allowance does not reflect the amendment to the title made on August 30, 2005. Please amend the title, according to this prior amendment, as follows:

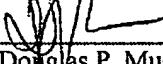
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE MEMORY DEVICE
HAVING ADDRESS GENERATING CIRCUIT USING PHASE ADJUSTMENT BY
SAMPLING DIVIDED CLOCK TO GENERATE ADDRESS SIGNAL OF SEVERAL
BITS HAVING ONE BIT CHANGED IN SEQUENTIAL ORDER

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record, Douglas P. Mueller (Reg. No. 30,300), at (612)455.3804.

Respectfully submitted,



HAMRE, SCHUMANN, MUELLER &
 LARSON, P.C.
 P.O. Box 2902-0902
 Minneapolis, MN 55402-0903
 (612) 455-3800

By: 
 Douglas P. Mueller
 Reg. No. 30,300
 DPM/acp

Dated: November 9, 2005